10/538923

JC17 Rec'd PCT/PTO 13 JUN 2005

Docket No.: 4590-421

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Frederic NGUYEN VAN DAU

U.S. Patent Application No. not yet assigned

Filed: herewith

HIGH PERFORMANCE SPIN-VALVE TRANSISTOR

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

For:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

This Application is related to U.S. Application Serial No. 10/484,481 filed on July 26, 2002.

Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above-identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. A copy of the foreign search report is attached for the Examiner's information. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

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Date: June 13, 2005

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION

ATTY. DOCKET NO. 4590-421

U.S. APPLICATION NO.
Not yet assigned QQ

APPLICANT

Frederic NGUYEN VAN DAU

FILING DATE

GROUP

herewith (PTO-1449) U.S. PATENT DOCUMENTS FILING **EXAMINER'S SUBCLASS** CLASS NAME DATE INITIALS PATENT NO. DATE 2000-05-30 Kishi Tatsuya et al 6, 069,820 A Sato Rie et al 2003/214004 A1 2003-11-20 Sato et al 2003/122208 A1 2003-07-03 4 4 1 6 7 FOREIGN PATENT DOCUMENTS Translation **EXAMINER'S SUBCLASS** CLASS COUNTRY PATENT NO. DATE INITIALS JP 2003-02-05 2002 305335A 2002 329902 A 2000-11-21 JP OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Dijken Van S et al: "Room temperature operation of a high output current magnetic tunnel transistor" Applied physics letters, American Institute of Physics, NY May 6, 2002 pages 3364-3366 Monsma D J et al: "Perpendicular Hot Electron Spin-Valve Effect in a New Magnetic Field Sensor: The Spin-Valve Transistor" Physical Review Letters, NY June 26, 1995 pages 5260-5263 Kumar P S et al: "The spin-valve transistor" Journal of Physics D (Applied physics) November 21, 2000 IOP Publishing, UK Sato R et al: "Spin-Valve Transistor Formed on Gaas (001) Substrate" IEEE Transactions on Magnetics, IEEE Inc. NY US September 2002 pages 2863-2868 M. Guth et al "Tunnel magnetoresistance in magnetic tunnel junctions with a ZnS barrier" Applied Physics Letters May 28, 2001 P. Rottlander et al "Tantalum oxide as an alternative low height tunnel barrier in magnetic junctions"

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

DATE CONSIDERED

Applied Physics Letters May 21, 2001

EXAMINER